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Description

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Method for filling a contact hole and integrated circuit arrangement with contact hole

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The invention relates to a method for filling a contact hole, in which a covering layer is deposited in the contact hole, which covering layer contains a nitride as main constituent.

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The contact hole lies between two metal layers. Such contact holes are also referred to as vias.

15 The covering layer is part of a so-called liner layer which serves as a mechanical adhesion promoting layer between a metal to be contact-connected and the contact hole filling. Titanium nitride or tantalum nitride, for example, is used as material for the adhesion promoting layer in order, for example in the case of a line section made of aluminium or an aluminium alloy and lying below the contact hole, to fill the contact hole with tungsten or a tungsten compound. Adhesion promoting layers which contain a nitride have good adhesion promoting properties and can be produced 20 comparatively simply by sputtering under nitrogen.

25 It is an object of the invention to specify a simple method for filling a contact hole which, in particular, ensures reliable contact connection and a low contact resistance. Moreover, the intention is to specify an associated integrated circuit arrangement.

30 The object relating to the method is achieved by means of the method steps specified in Patent Claim 1.
35 Developments are specified in the subclaims.

In the case of the method according to the invention, a base layer is deposited in the contact hole under a protective gas, which base layer contains a nitride as

main constituent. It is only after the deposition of the base layer that the covering layer is then deposited under gaseous nitrogen.

5 By virtue of the fact that firstly the base layer is deposited under a protective gas, no disturbing nitride compounds between the metal at the bottom of the contact hole and nitrogen contained in a reactive gas form on the metal at the bottom of the contact hole in
10 the case of the method according to the invention. A metal nitride layer is nevertheless formed directly on the metal in the case of the method according to the invention, in order e.g. to form a good electrical contact and to avoid or to reduce undesirable effects
15 or compounds between a pure metal and the metal at the bottom of the contact hole. Thus, e.g. $TiAl_3$, would be disturbing because it has a density which deviates considerably from the density of the materials surrounding it. Moreover, $TiAl_3$, is granular and has a
20 non-uniform structure which fosters electromigration.

During the subsequent deposition of the covering layer and a gaseous nitrogen, on account of the base layer that has already been deposited, the nitrogen can no
25 longer penetrate as far as the metal at the bottom of the contact hole in order to form a disturbing nitride there. Therefore, the covering layer can be produced by a simple method for deposition, namely that using a nitrogen atmosphere.

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In one development of the method according to the invention, the base layer and the covering layer are deposited by directional sputtering. Whereas non-directional sputtering methods are well suited to the
35 deposition of material on a planar area, directional sputtering affords the possibility of depositing sufficient material of the base layer and of the covering layer even in the narrow contact hole, and in particular on the bottom of the contact hole, without

resulting in excessively large differences in the thickness of the base layer and of the covering layer inside and outside the contact hole. Since such differences are avoided or considerably reduced by 5 directional sputtering, the method in accordance with the development can be incorporated in a simple manner into the overall process for producing an integrated circuit arrangement.

10 In a next development of the method according to the invention, an intermediate layer is deposited in the contact hole after the deposition of the base layer but before the deposition of the covering layer. The deposition of the intermediate layer affords the 15 possibility of effecting continuous transition from the method for depositing the base layer to the method for depositing the covering layer. By way of example, process gases can be exchanged in this time. Moreover, incorporating an intermediate layer with a material 20 which differs from the material of the base layer and from the material of the covering layer, because it is nitride-free, results in a degree of freedom for producing a layer stack with improved mechanical adhesion properties and/or with improved electrical 25 contact properties and/or with improved other properties, e.g. with regard to the prevention of diffusion or electromigration. Thus, it is possible to considerably improve the adhesion properties of the contact hole filling that is to be introduced later, in 30 the contact hole itself, by means of an intermediate layer which contains titanium as main constituent, i.e. which comprises e.g. titanium or in which at least 80% of the atoms are titanium atoms.

35 In other developments, the surface of a sputtering target is firstly nitrided under a nitrogen atmosphere. The nitrided surface is then removed during the production of the base layer. The intermediate layer is then removed from an essentially nitride-free surface

of the sputtering target, the protective gas remaining unchanged. The conditions for producing the covering layer are then prescribed by the change to a different atmosphere. These measures form a simple but effective

5 method for prescribing the process conditions during the production of the layer stack for filling the contact hole. In particular, the entire method can be carried out without changing the process chamber.

10 In a next development, the contact hole is introduced into a dielectric carrier material as far as an electrically conductive connecting section which contains, as main constituent, aluminium, an aluminium alloy or a different metal, e.g. copper or a copper

15 alloy. Such a deeply penetrating contact hole, too, can be filled well by the method according to the invention, in particular because no gaseous nitrogen reaches the bottom of the contact hole. Thus, by way of example, the formation of disturbing aluminium nitride,

20 as explained above, is avoided by other measures, namely in particular by the production of the base layer under a protective atmosphere.

In a next development, a multiplicity of contact holes

25 are etched simultaneously during the processing of a semiconductor wafer. The semiconductor wafer has a diameter of 150 to 300 mm, for example. This size means that the etching conditions are not identical at all locations of the semiconductor wafer. By way of

30 example, etching takes place more rapidly at the edge of the semiconductor wafer than in the centre of the semiconductor wafer. The thickness of the dielectric layer outside contact holes also varies at different locations of the semiconductor wafer. By virtue of the

35 method according to the invention, it is possible that, in the case of the development, although an auxiliary layer lying between the dielectric layer and a metal layer is prescribed as a desired point for an etching stop, the auxiliary layer is nonetheless permitted to

be etched through, in particular in a partial region of the semiconductor wafer. All of the contact holes subsequently have good electrical properties despite the different etching conditions and thickness
5 conditions. In particular in the case of a low etching selectivity between the dielectric layer and the auxiliary layer, the measures mentioned enable a large process window during the etching. The thickness of the auxiliary layer can also be reduced.

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The auxiliary layer has properties, for example, which make it possible to avoid or reduce electromigration. Even if the auxiliary layer is etched through, this is not disturbing because later a liner layer is applied
15 which has similar properties to the auxiliary layer with regard to avoiding electromigration. The similar properties result e.g. from the use of identical materials and layer sequences in the auxiliary layer and the liner layer.

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In another development, a contact hole filling is deposited after the deposition of the covering layer in the contact hole, which contact hole filling contains tungsten as main constituent. Even when the
25 intermediate layer is composed of titanium, the formation of disturbing titanium fluoride during the introduction of the tungsten can be avoided or considerably reduced because the intermediate layer is embedded between the base layer and the covering layer
30 and, moreover, can be made so that it is only thin, e.g. thinner than 10 nm.

In a next development, the thickness of the covering layer is less than about 20 nm. Such a small thickness
35 is not disturbing even when the contact hole is filled with tungsten. The formation of titanium fluoride and the resultant lifting-off of the layer stack from the contact hole need no longer be feared. Therefore, the layer thickness, in particular of the covering layer,

which is otherwise enlarged in order to avoid the lifting-off can also be reduced again, for example to the size mentioned.

5 In one development, the contact hole has a diameter of less than 1 µm. The depth of the contact hole is greater than 500 nm. Given a diameter of about 500 nm and a depth of 1 µm, the aspect ratio is two. However, even with aspect ratios of up to about three, the
10 method according to the invention can still be carried out reliably because, particularly in the case of a directional sputtering method, the difference in thickness of the layers inside and outside a contact hole remains within tenable limits.

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In a next development, one or more of the materials titanium nitride or tantalum nitride is used as material for the base layer and/or the covering layer. In these cases, titanium or tantalum is suitable as
20 material for the intermediate layer.

The invention additionally relates to an integrated circuit arrangement which contains the filled contact hole, in particular including the base layer and the
25 covering layer. In one development, the circuit arrangement is produced by the method according to the invention or one of its developments. Thus, the effects mentioned above also apply to the circuit arrangement and the development thereof.

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The invention is explained below with reference to the accompanying figures, in which:

Figure 1 shows a dielectric layer of an integrated
35 circuit arrangement,

Figure 2 shows a contact hole etched into the dielectric layer,

Figure 3 shows an adhesion promoting layer to be arranged in the contact hole, in accordance with one variant,

5 Figure 4 shows partial layers of an adhesion promoting layer introduced into the contact hole,

Figure 5 shows a sputtering chamber used for introducing the adhesion promoting layer, and

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Figure 6 shows method steps carried out during the production of the adhesion promoting layer.

Figure 1 shows an integrated circuit arrangement 10 during production. In a semiconductor substrate (not illustrated) of the integrated circuit arrangement 10, a multiplicity of electrical components such as transistors have already been fabricated, e.g. in accordance with CMOS technology, in accordance with 15 BICMOS technology or in accordance with a technology for power switching elements (power devices). The production was then continued until the application of 20 a metal layer 12.

25 The metal layer 12 contains a connecting section 14 made of an aluminium-copper alloy containing e.g. 0.5% copper. An antireflection layer 16 comprising titanium nitride, for example, or containing at least one titanium nitride layer was sputtered onto the metal 30 layer 12. The antireflection layer 16 was required for patterning the metal layer 12 in a photolithographic process during which the connecting section 14 was also patterned.

35 After the deposition of the antireflection layer 16, a dielectric layer 18 was deposited with a thickness of 600 nm, for example, e.g. with the aid of a CVD method (Chemical Vapour Deposition). The dielectric layer comprises silicon dioxide, for example, and serves for

electrical insulation between the metal layer 12 and a metal layer that is still to be arranged in the dielectric layer 18.

5 Figure 2 shows the circuit arrangement 10 after the etching of a contact hole 20, which extends through the dielectric layer 18 and the antireflection layer 16 right into the connecting section 14. Between a lower surface 22 and a contact hole bottom 24 of the contact
10 hole 20 there is a distance A1 of 10 nm, for example. The diameter of the contact hole is 0.5 μm , for example.

15 The etching process for etching the contact hole 20 is conducted such that, for a large part of contact holes of the circuit arrangement 10, the bottom 26 of the contact hole lies in the centre of the antireflection layer 16. Between the bottom 26 of the contact hole and the lower surface 22 of the antireflection layer 16
20 there is then a distance A2 of a few nanometres. In this process implementation, there are no contact holes in which the bottom 28 of the contact hole lies above the antireflection layer 16. Between the bottom 28 of the contact hole and the lower surface 22 there would
25 then be a distance A3 which is greater than the distance A2 and also greater than the thickness of the antireflection layer 16. The contact hole 20 has a central region 30, which is shown in enlarged illustration in Figures 3 and 4.

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After the etching of the contact hole 20, an adhesion promoting layer 32 is deposited, the construction of which is explained in more detail below with reference to Figure 4.

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Figure 3 shows a titanium nitride layer 40 which could be deposited in the contact hole 20 as adhesion promoting layer. If this were done under a reactive nitrogen atmosphere, then an aluminium nitride layer 42

would form between the titanium nitride layer 40 and the connecting section 14, which aluminium nitride layer considerably increases the contact resistance.

5 By contrast, Figure 4 shows the construction of the adhesion promoting layer 32 actually deposited in the contact hole 20, the said adhesion promoting layer containing a base layer 50 made of titanium nitride, an intermediate layer 52 and a covering layer 54 made of
10 titanium nitride. The base layer 50, the intermediate layer 52 and the covering layer 54 were sputtered in the order mentioned by a method which is explained in more detail below with reference to Figure 6.

15 The intermediate layer 52 comprises a mixture of titanium nitride and titanium in lower regions B1 and B2, the proportion of titanium increasing in the regions B1 and B2, starting from the base layer 50, and reaching 100% in a region B3 adjoining the region B2.
20 Equally, the proportion of titanium nitride decreases from 100% to 0%. The proportion of titanium in the centre of the region B1 and of the region B2 is, for example, 60% and 90%, respectively. The proportion of titanium is also 100% in a region B4 lying above the
25 region B3. The regions B1 to B2 have identical thicknesses D1 of 0.5 nm, for example, so that a total thickness D2 of the intermediate layer 52 is 2 nm. A thickness D3 of the base layer 50 is 3 nm in the exemplary embodiment. A thickness D4 of the covering layer is 10 nm. The thicknesses D1 to D4 relate to the extent of the layers in a stack direction R, in which the layers 50 to 54 are stacked one above the other and which lies at right angles to the surface of the semiconductor substrate.

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Figure 5 shows a sputtering chamber 100 used for introducing the adhesion promoting layer 32. A receptacle 102 has a gas inlet 104 and a gas outlet 106. The receptacle additionally contains a sputtering

target 108 made of titanium, which serves as cathode 107 and a wafer holder 110, which serves as anode 109. The wafer holder 110 carries a wafer 112, e.g. an 8 inch wafer (1 inch = 25.4 mm). The sputtering target 5 108 has e.g. the same diameter as the wafer.

The sputtering chamber 100 is suitable for directional sputtering because a distance A between sputter target 108 and wafer 112 has been considerably increased in 10 comparison with a sputtering chamber for non-directional sputtering, for example by a factor of four to five. Thus, the distance A is about 25 cm in the exemplary embodiment. Between the connecting line from a point P in the centre of the wafer 112 toward the 15 edge of the sputtering target 108 and the normal N to the main surface of the wafer 112 there is an angle W, which is less than 45°, in particular less than 30°, in the case of directional sputtering.

20 However, directional sputtering can also be achieved or enhanced by measures other than a large distance A, e.g. by reducing the pressure within the sputtering chamber 100, e.g. to only 1 to 2 millitorr, or by suitable bias voltages during sputtering. Other methods 25 also lead to directional sputtering, e.g.:
- the use of an IMP method (Ionized Metal Plasma) from the company Applied Materials,
- the use of an SIP method (Self Ionized Plasma) from the company Applied Materials,
30 - the use of the Advanced High Fill method from the company Trikon,
- the use of the Ultra High Fill method from the company Trikon,
- or the use of older sputtering with a collimator.

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Thus, directional sputtering can be differentiated from non-directional sputtering by an angle W of less than 45° or less than 30° or else by other measures which lead to the same effect as a small angle W with regard

to the ratio of the layer thicknesses inside and outside a contact hole 20.

Figure 6 shows method steps carried out during the production of the adhesion promoting layer 32. The method begins in a method step 150. In a method step 152, the sputtering target 108 is inserted into the sputtering chamber 100 in order to utilize it for a multiplicity of sputtering processes. The sputtering target 108 contains a titanium layer 153 made of pure titanium.

In a subsequent method step 154, nitrogen gas is introduced into the sputtering chamber 100. The nitrogen brings about a nitriding of the reactive titanium layer 153. A thin titanium nitride layer 157 is therefore produced at the surface of the titanium layer 153.

After the nitriding, in a method step 158, the supply of nitrogen is interrupted and the nitrogen contained in the sputtering chamber 100 is extracted by suction. In a next method step 106, the wafer 112 is fixed on the wafer holder 110 in the sputtering chamber.

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In a method step 162, a protective gas, for example argon, is introduced into the sputtering chamber 100. Under the argon atmosphere that forms, sputtering is begun in a method step 164, the base layer 50 being deposited on the wafer 112. If the last parts of the titanium nitride layer 157 and then parts of the titanium layer 153 are sputtered away, then the intermediate layer 52 is likewise formed under the argon atmosphere.

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After the deposition of the intermediate layer 52, in a method step 166, nitrogen is introduced into the sputtering chamber 100 in addition to the protective gas or instead of the protective gas. The sputtering

can be interrupted in this case in order to produce reproducible layers. In a method step 168, the sputtering is continued by reigniting the plasma, the covering layer 54 being formed. In a method step 170,
5 the method is ended if the covering layer 54 and thus also the adhesion promoting layer 32 have reached their predetermined thickness.

Without changing the sputtering target 108, the method
10 explained is carried out a number of times in succession.

Later, in a different chamber, tungsten is introduced into the contact hole that has already been lined with
15 the adhesion promoting layer 32. Afterwards, still further metal layers of the integrated circuit arrangement 10 are produced.

List of reference symbols

10	Circuit arrangement
12	Metal layer
14	Connecting section
16	Antireflection layer
18	Dielectric layer
20	Contact hole
22	Lower surface
24	Bottom of contact hole
A1 to A3	Distance
26, 28	Bottom of contact hole
30	Central region
32	Adhesion promoting layer
40	Titanium nitride layer
42	Aluminium nitride layer
50	Base layer
52	Intermediate layer
54	Covering layer
B1 to B4	Regions of the covering layer
D1 to D4	Thickness
R	Stack direction
100	Sputtering chamber
102	Receptacle
104	Gas inlet
106	Gas outlet
107	Cathode
108	Sputtering target
109	Anode
110	Wafer holder
112	Wafer
A	Distance
P	Point
W	Angle
N	Normal
150	Start
152	Titanium target in chamber
153	Titanium layer
154	Introduce nitrogen

156 Nitriding of the target
157 Titanium nitride layer
158 Remove nitrogen
160 Introduce wafer
162 Introduce argon
164 Sputtering
166 Introduce nitrogen
168 Further sputtering
170 End